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Symmetric multiprocessor (SMP) servers provide superior performance for the commercial workloads that dominate the Internet. Our simulation results show that over one-third of cache misses by these applications result in cache-to-cache transfers, where ...


## 5 C<sup>2</sup>MP: a cache-coherent, distributed memory multiprocessor-system



D. E. Marquardt, H. S. Alkhatib

August Supercomputing '89: Proceedings of the 1989 ACM/IEEE conference on Supercomputing

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Current research into the problems of cache coherency in multiprocessor (MP) systems, has primarily focused on bus based memory interconnection networks (M-ICN) and the use of various types of "snooping" ...

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